

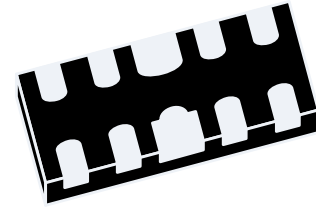
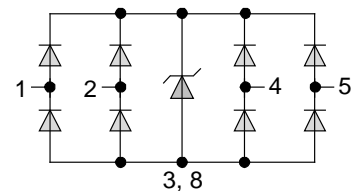
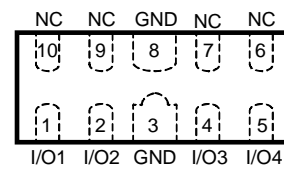
**ESD5324D**
**4-Lines, Uni-directional, Ultra-low Capacitance  
Transient Voltage Suppressors**
<http://www.sh-willsemi.com>
**Descriptions**

The ESD5324D is an ultra-low capacitance TVS (Transient Voltage Suppressor) array designed to protect high speed data interfaces. It has been specifically designed to protect sensitive electronic components which are connected to data and transmission lines from over-stress caused by ESD (Electrostatic Discharge).

The ESD5324D incorporates four pairs of ultra-low capacitance steering diodes plus a TVS diode.

The ESD5324D may be used to provide ESD protection up to  $\pm 30\text{kV}$  (contact discharge) according to IEC61000-4-2, and withstand peak pulse current up to 6A (8/20 $\mu\text{s}$ ) according to IEC61000-4-5.

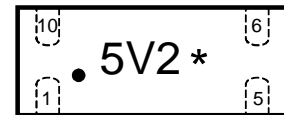
The ESD5324D is available in DFN2510-10L package. Standard products are Pb-free and Halogen-free.


**DFN2510-10L (Bottom view)**

**Pin configuration (Top view)**
**Features**

- Stand-off voltage: 5V max.
- Transient protection for each line according to IEC61000-4-2 (ESD):  $\pm 30\text{kV}$  (contact discharge)  
IEC61000-4-4 (EFT): 40A (5/50ns)  
IEC61000-4-5 (surge): 6A (8/20 $\mu\text{s}$ )
- Ultra-low capacitance:  $C_J = 0.65\text{pF}$  typ.
- Ultra-low leakage current:  $I_R < 1\text{nA}$  typ.
- Low clamping voltage:  $V_{CL} = 14.5\text{V}$  typ. @  $I_{TLP} = 16\text{A}$
- Solid-state silicon technology

**Applications**

- HDMI
- SATA and eSATA
- DVI
- IEEE 1394
- PCI Express
- Portable Electronics and Notebooks



5V2 = Device code

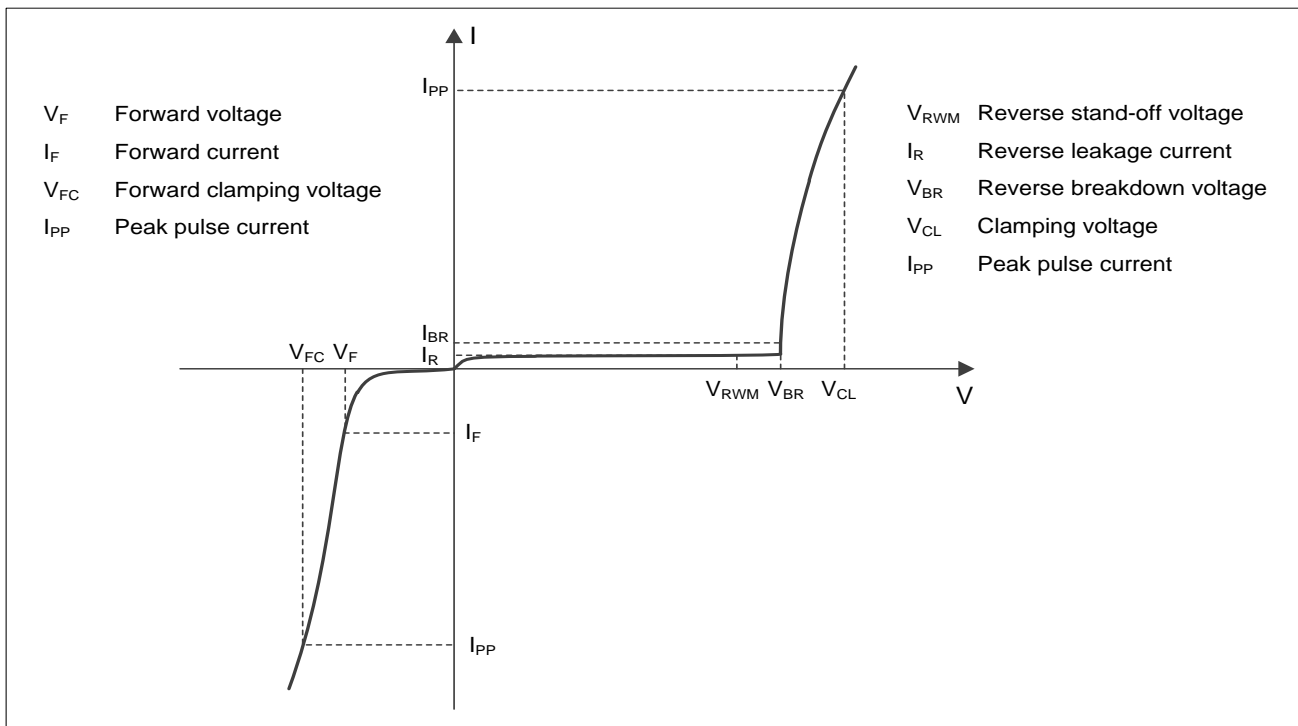
\* = Month code ( A~Z)

**Marking**
**Order information**

Device	Package	Shipping
ESD5324D-10/TR	DFN2510-10L	3000/Tape&Reel

**Absolute maximum ratings**

Parameter	Symbol	Rating	Unit
Peak pulse power ( $t_p = 8/20\mu s$ )	$P_{pk}$	80	W
Peak pulse current ( $t_p = 8/20\mu s$ )	$I_{PP}$	6	A
ESD according to IEC61000-4-2 air discharge	$V_{ESD}$	$\pm 30$	kV
ESD according to IEC61000-4-2 contact discharge		$\pm 30$	
Junction temperature	$T_J$	125	$^{\circ}C$
Operating temperature	$T_{OP}$	-40~85	$^{\circ}C$
Lead temperature	$T_L$	260	$^{\circ}C$
Storage temperature	$T_{STG}$	-55 to +150	$^{\circ}C$

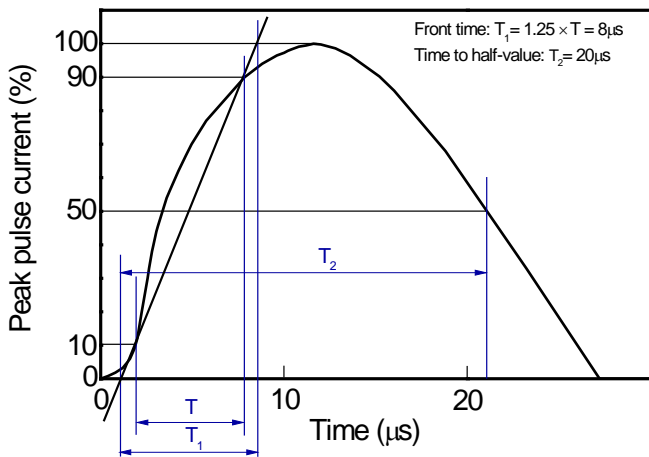
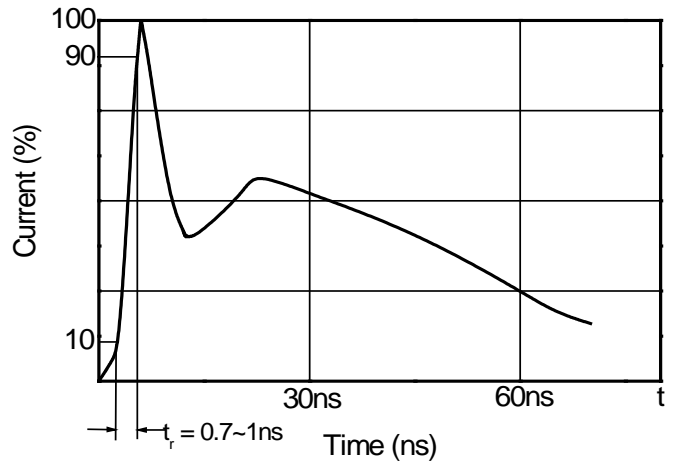
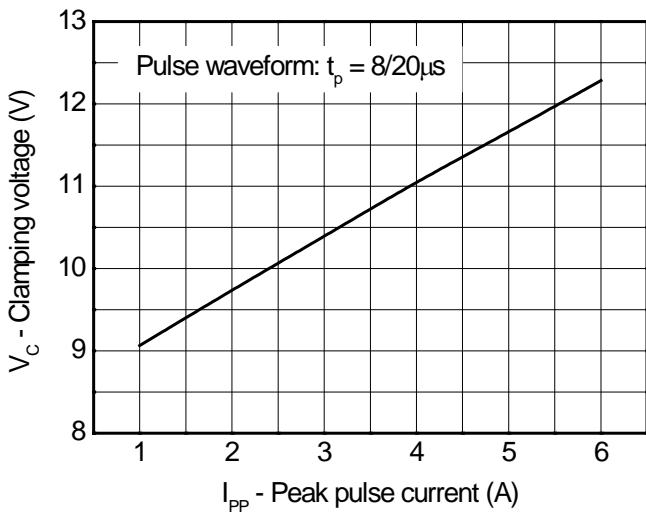
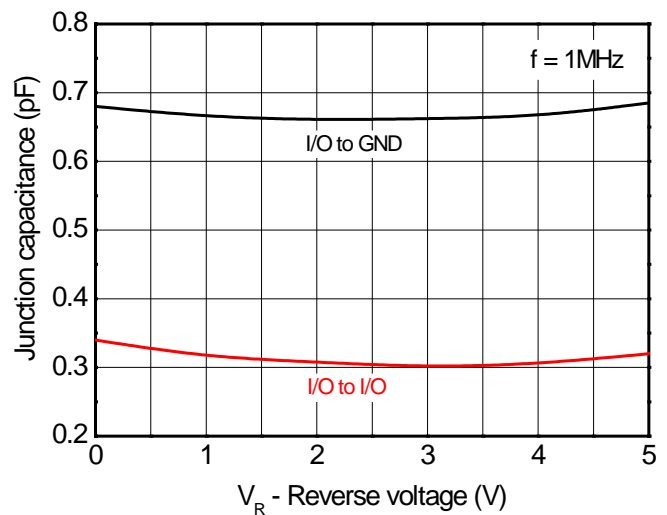
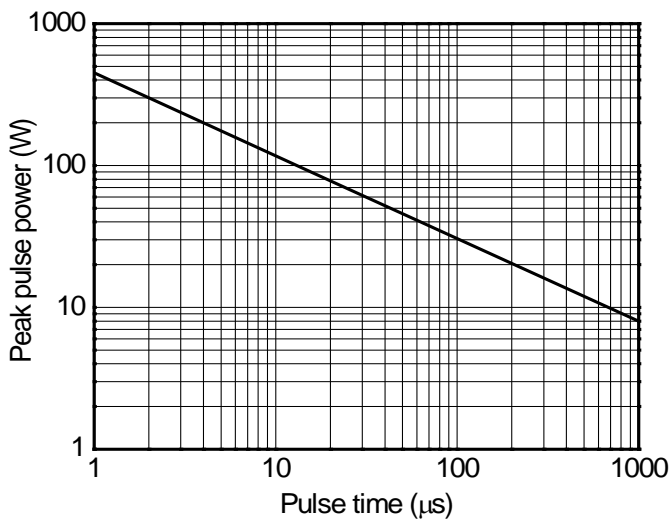
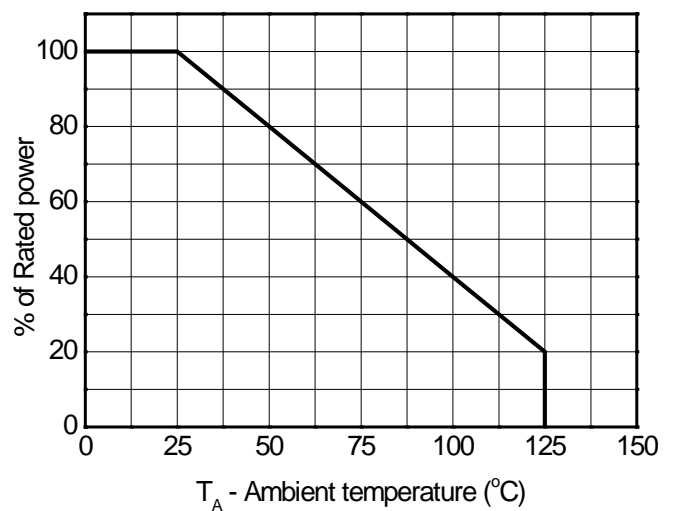
**Electrical characteristics ( $T_A = 25^{\circ}C$ , unless otherwise noted)**

**Definitions of electrical characteristics**

**Electrical characteristics ( $T_A = 25^\circ\text{C}$ , unless otherwise noted)**

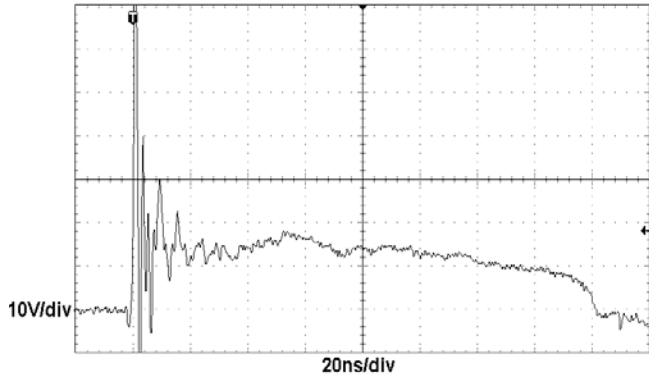
Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Reverse maximum working voltage	$V_{RWM}$				5.0	V
Reverse leakage current	$I_R$	$V_{RWM} = 5\text{V}$		<1	100	nA
Reverse breakdown voltage	$V_{BR}$	$I_T = 1\text{mA}$	7.0	8.0	9.0	V
Forward voltage	$V_F$	$I_T = 10\text{mA}$	0.6	0.9	1.2	V
Clamping voltage <sup>1)</sup>	$V_{CL}$	$I_{PP} = 16\text{A}$ , $t_p = 100\text{ns}$		14.5		V
Dynamic resistance <sup>1)</sup>	$R_{DYN}$			0.32		$\Omega$
Clamping voltage <sup>2)</sup>	$V_{CL}$	$I_{PP} = 1\text{A}$ , $t_p = 8/20\mu\text{s}$			10	V
		$I_{PP} = 6\text{A}$ , $t_p = 8/20\mu\text{s}$			13	V
Junction capacitance	$C_J$	$V_R = 0\text{V}$ , $f = 1\text{MHz}$ Any I/O pin to GND		0.65	0.85	pF
		$V_R = 0\text{V}$ , $f = 1\text{MHz}$ Between any I/O pin		0.35	0.50	pF

**Notes:**

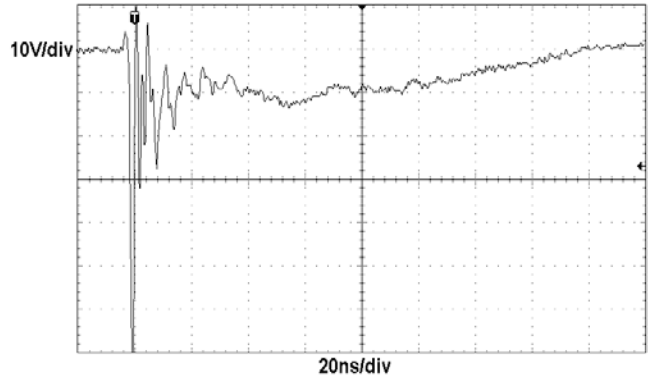
- 1) TLP parameter:  $Z_0 = 50\ \Omega$ ,  $t_p = 100\text{ns}$ ,  $t_r = 2\text{ns}$ , averaging window from 60ns to 80ns.  $R_{DYN}$  is calculated from 4A to 16A.
- 2) Non-repetitive current pulse, according to IEC61000-4-5.

**Typical characteristics ( $T_A = 25^\circ\text{C}$ , unless otherwise noted)**

**8/20μs waveform per IEC61000-4-5**

**Contact discharge current waveform per IEC61000-4-2**

**Clamping voltage vs. Peak pulse current**

**Capacitance vs. Reverse voltage**

**Non-repetitive peak pulse power vs. Pulse time**

**Power derating vs. Ambient temperature**

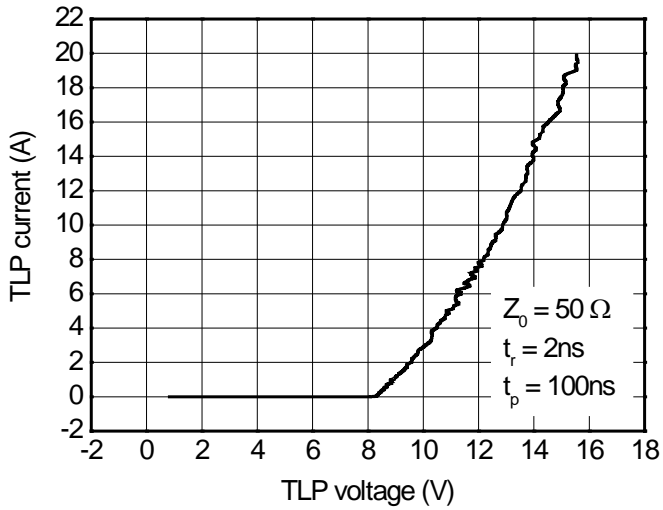
Typical characteristics ( $T_A = 25^\circ\text{C}$ , unless otherwise noted)



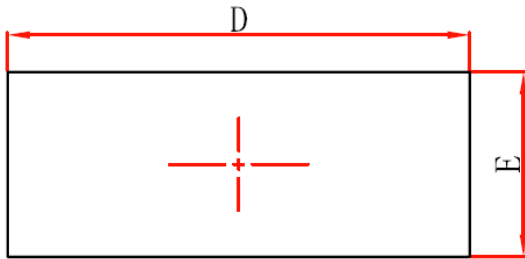
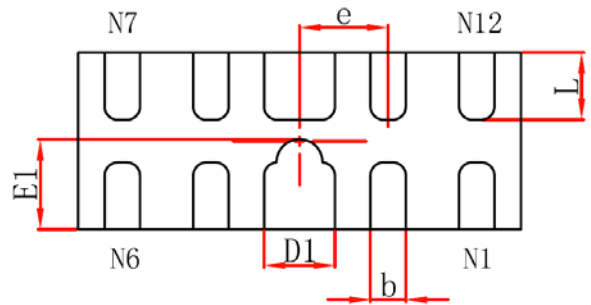
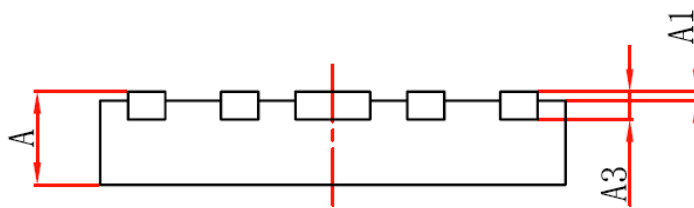
**ESD clamping**  
 (+8kV contact discharge per IEC61000-4-2)



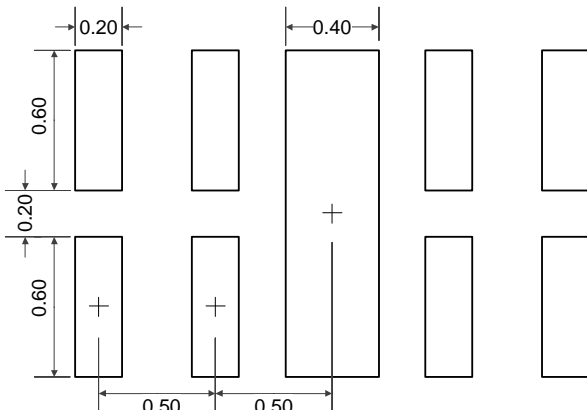
**ESD clamping**  
 (-8kV contact discharge per IEC61000-4-2)



**TLP Measurement**

**Package outline dimensions**
**DFN2510-10L**

**Top View**

**Bottom View**

**Side View**

Symbol	Dimensions in millimeter		
	Min.	Typ.	Max.
A	0.550	0.600	0.650
A1	0.000	-	0.050
A3	0.150 Ref.		
D	2.424	2.500	2.576
E	0.924	1.000	1.076
D1	0.300	0.400	0.500
E1	0.410	0.510	0.610
b	0.150	0.200	0.250
e	0.500 Typ.		
L	0.304	0.380	0.456

**Recommend Land Pattern (Unit: mm)**

**Notes:**

This recommended land pattern is for reference purposes only. Please consult your manufacturing group to ensure your PCB design guidelines are met.